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ANALOG CIRCUIT DESIGN VOLUME 2

CHAPTER 32. MEASUREMENT AND CONTROL CIRCUIT COLLECTION: DIAPERS AND DESIGNS ON THE NIGHT SHIFT

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THE AIR FORCE GEOPHYSICS LABORATORY STANDALONE DATA ACQUISITION SYSTEM

A FUNCTIONAL DESCRIPTION

The Standalone Data Acquisition System (SDAS) is a portable digital data recording system for geophysical field data collection. This report describes the SDAS, giving special emphasis to the SDAS signal conditioning subsystem, which uses custom-designed active filter modules for signal filtering and amplifications. Detailed descriptions of the active filters, including design equations and software, are provided. This subsystem, together with software for its configuration and performance verification, provides an improved capability for analog signal conditioning. This report also provides a functional description of the SDAS. It documents the system hardware and such software as is needed for system configuration and checkout. Each subsystem in the SDAS is described via functional descriptions and such diagrams and schematics as are necessary to understand subsystem functioning. Detailed schematics, parts lists, software listings, and subsystem specifications are covered in appendices.

TECHNICAL ABSTRACT BULLETIN

ELECTRONICS INDUSTRY

NINETEENTH ANNUAL CONFERENCE ON MANUAL CONTROL

LABOR RELATIONS REFERENCE MANUAL

LABOR RELATIONS MASTER TABLE OF CASES

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ENERGY RESEARCH ABSTRACTS

ELECTRONIC DESIGN

A DIRECTORY OF COMPUTER SOFTWARE APPLICATIONS

MINICOMPUTERS & MICROCOMPUTERS, AUGUST 1977-1980

WIDEBAND AMPLIFIERS

Springer Science & Business Media This work covers two bases, both performance optimization strategies and a complete introduction to mathematical procedures required for a successful circuit design. It starts from the basics of mathematical procedures and circuit analysis before moving on to the more advanced topics of system optimization and synthesis, along with the complete mathematical apparatus required. The authors have been at pains to make the material accessible by limiting the mathematics to the necessary minimum.

USING MICROPROCESSORS AND MICROCOMPUTERS

THE MOTOROLA FAMILY

Pearson College Division Reflecting the most recent changes in the ever advancing Motorola Mictrocontroller market, this leading book for the Motorola family of microprocessors remains at the forefront of its field. Discussing today's most significant trends in the microcomputer and microcontroller worlds, it builds upon traditional coverage of 8-bit technology to include the exciting applications of Motorola's microcontrollers - the 68HC11 family - and now goes beyond to include many new high-performance designs. Examines the fundamental concepts of the 68000 families of Microprocessors that are used as the basis of many new Microcontrollers, and offers complete chapter coverage on computer arithmetic and logic operations; accumulator and memory reference; assembly language for the 6800 family; interrupts and timing; system debugging; real-world applications and interfacing techniques, and much more. Now considers the many new developments in the area of memory, and includes new chapters on the 68HC08 family; the 16-bit 68HC12 MCU (with in-depth analyses on the revolutionary new system development methods); plus the radically new RISC type of MCU, the MPC505, that uses a 32-bit Power PC CPU.

COMPUTERS & ELECTRONICS

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COMPUTERS, CONTROL & INFORMATION THEORY

THE SOFTWARE ENCYCLOPEDIA

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WIRELESS WORLD

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